```
VV      VV   EEEEEEEEEE   RRRRRRRR      SSSSSSSS   77777777      333333      666666
VV      VV   EEEEEEEEEE   RRRRRRRR      SSSSSSSS   77777777      333333      666666
VV      VV   EE           RR     RR    SS                 77   33      33   66
VV      VV   EE           RR     RR    SS                 77   33      33   66
VV      VV   EE           RR     RR    SS                 77           33   66
VV      VV   EE           RR     RR    SS                 77           33   66
VV      VV   EEEEEEEE     RRRRRRRR      SSSSSS           77           33   66666666
VV      VV   EEEEEEEE     RRRRRRRR      SSSSSS           77           33   66666666
VV      VV   EE           RR  RR              SS        77             33   66      66
VV      VV   EE           RR  RR              SS        77             33   66      66
  VV  VV     EE           RR    RR            SS      77         33      33   66      66      ....
  VV  VV     EE           RR    RR            SS      77         33      33   66      66      ....
    VV       EEEEEEEEEE   RR      RR    SSSSSSSS     77           333333      666666        ....
    VV       EEEEEEEEEE   RR      RR    SSSSSSSS     77           333333      666666        ....

LL             SSSSSSSS   TTTTTTTTTT       ;;;;         11
LL             SSSSSSSS   TTTTTTTTTT       ;;;;         11
LL           SS               TT           ;;;;       1111
LL           SS               TT           ;;;;       1111
LL           SS               TT                        11
LL           SS               TT                        11
LL             SSSSSS         TT           ;;;;         11
LL             SSSSSS         TT           ;;;;         11
LL                   SS       TT           ;;;;         11
LL                   SS       TT           ;;;;         11
LL                   SS       TT             ;;         11
LL                   SS       TT             ;;         11
LLLLLLLLLL   SSSSSSSS         TT           ;;       111111
LLLLLLLLLL   SSSSSSSS         TT           ;;       111111
```

```
                    1  ********************************************************************************
                    2  ********************************************************************************
                    3  ********************************************************************************
                    4  ***                                                                          ***
                    5  ***                                                                          ***
                    6  ***                                                                          ***
                    7  ***            VERSION MODULE FOR  ISI  PRINTERS COMPATIBLE                  ***
                    8  ***                   with  IBM  3274/6 PROTOCOL                             ***
                    9  ***                                                                          ***
                   10  ***                                                                          ***
                   11  ***                                                                          ***
                   12  ***                   INTERFACE SYSTEMS, INC.                                ***
                   13  ***                   ANN ARBOR, MICHIGAN  48103                             ***
                   14  ***                                                                          ***
                   15  ***                                                                          ***
                   16  ***                                                                          ***
                   17  ***            FOR MOTOROLA  6809  OR COMPATIBLE MICROPROCESSOR              ***
                   18  ***                   USING A GENERATION III INTERFACE                       ***
                   19  ***                                                                          ***
                   20  ***                                                                          ***
                   21  ***                                                                          ***
                   22  ********************************************************************************
                   23  ********************************************************************************
                   24  ********************************************************************************
                   25  *
                   26  *
                   27  *                   BY:     INTERFACE SYSTEMS, INCORPORATED
                   28  *                           462 JACKSON PLAZA
                   29  *                           ANN ARBOR, MICHIGAN  48103
                   30  *
                   31  *                           RICHARD L. COLE
                   32  *
                   33  *                           COPYRIGHT 1982, 1983
                   34  *                                   BY
                   35  *                           INTERFACE SYSTEMS, INC.
                   36  *
                   37  *                           ALL RIGHTS RESERVED
                   38  *
                   39  *
                   40  ********************************************************************************
                   41  ********************************************************************************
                   42  *
                   43  *
                   44            NAM     VERSION MODULE
                   45  *
                   46  *
                   47            INCLUD  DK3:[140,2]RANGE.ACT
                   48  *
=2000              49  .RAMB.    EQU     $2000           target/target variable RAM
=27FF              50  .RAME.    EQU     $27FF
=C000              51  .ROMB.    EQU     $C000           target program base address
=FFFF              52  .ROME.    EQU     $FFFF           target program limit
```

```
                                53   *
                                54   *
                                55   *
=C000 =FFFF                     56        SECT    VERS,REL,RANGE=.ROMB.:.ROME.
                                57   *
                                58   *
                                59   *       the following is a device module compatible buffer containing
                                60   *            version information which can be transferred to the device
                                61   *            buffer and printed.  additional information for the version
                                62   *            line is synthesized by the test routine on the fly.
                                63   *            additional information is asumed to contain the model number
                                64   *            and checksum following the printer type.
                                65   *
                                66   *
                                67   *       the following table of characters defines the codes for the
                                68   *            decimal digits
                                69   *
=0020                           70   K0      EQU     $20             zero
=0021                           71   K1      EQU     $21             one
=0022                           72   K2      EQU     $22             two
=0023                           73   K3      EQU     $23             three
=0024                           74   K4      EQU     $24             four
=0025                           75   K5      EQU     $25             five
=0026                           76   K6      EQU     $26             six
=0027                           77   K7      EQU     $27             seven
=0028                           78   K8      EQU     $28             eight
=0029                           79   K9      EQU     $29             nine
                                80   *
                                81   *
      =0000                     82        RSECT   VERS
                                83   *
                                84   *
                                85        INTERN  VERSIO
                                86        INTERN  CNFL1
                                87   *
                                88   *
0000' 00 00 00 00 00 00         89        FCB     0,0,0,0,0,0,0,0         device requires 16 bytes before count
0006' 00 00
0008' 00 00 00 00 00 00         90        FCB     0,0,0,0,0,0,12,0        12 is increments/char for 10 pitch
000E' 0C 00
0010' 3E                        91   VERSIO FCB     62                      byte count in this buffer
      =0010                     92        RADIX   16                      switch to hex for characters
0011' A2 8E 8F 98 91 88         93        FCB     $A2,8E,8F,98,91,88,86,87,93,10          Copyright
0017' 86 87 93 10
001B' 20 25 14 21 20 14         94        FCB     K0,K5,14,K1,K0,14,K8,K3,10              05/10/83
0021' 28 23 10
0024' A8 8D 93 84 91 85         95        FCB     $A8,8D,93,84,91,85,80,82,84,10          Interface
002A' 80 82 84 10
002E' B2 98 92 93 84 8C         96        FCB     $B2,98,92,93,84,8C,92,33,10             Systems,
0034' 92 33 10
0037' A8 8D 82 32 10 A0         97        FCB     $A8,8D,82,32,10,$A0,8D,8D,10            Inc. Ann
003D' 8D 8D 10
```

```
0040'  A0 91 81 8E 91 33      98          FCB     $A0,91,81,8E,91,33,10,$AC,$A8,10        Arbor, MI
0046'  10 AC A8 10
004A'  27 23 26 B0 31         99          FCB     27,23,26,$B0,31                         736Q-
       =000A                 100          RADIX   10
                             101   *
004F'  FF                    102   CNFL1  FCB     $FF             device is a 736q
                             103   *
                             104          END
```

CNFL1	004F'IN	K0	0020	K1	0021	K2	0022	K3	0023
K4	0024	K5	0025	K6	0026	K7	0027	K8	0028
K9	0029	VERSIO	0010'IN	.RAMB.	2000	.RAME.	27FF	.ROMB.	C000
.ROME.	FFFF								

No errors detected